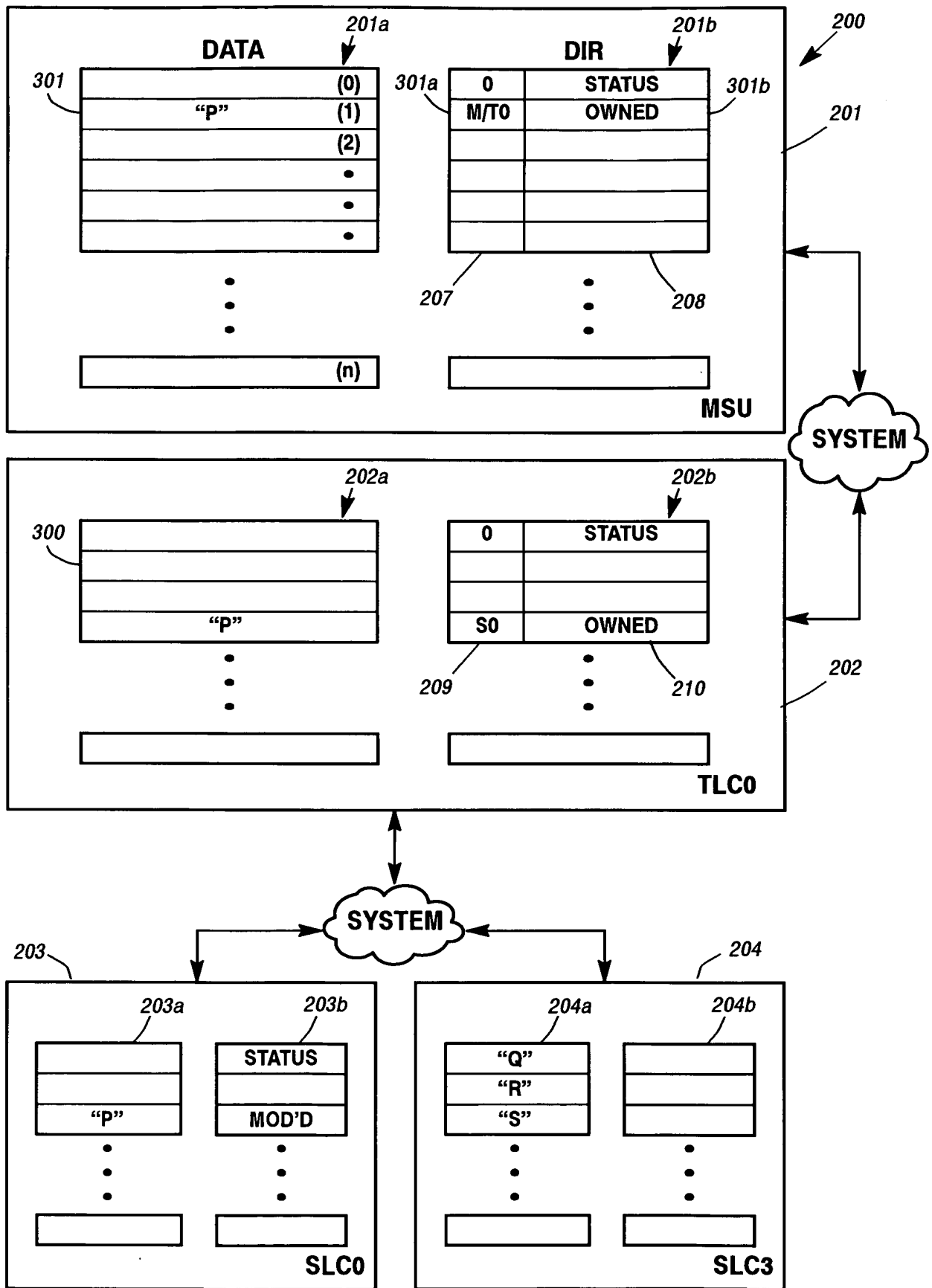
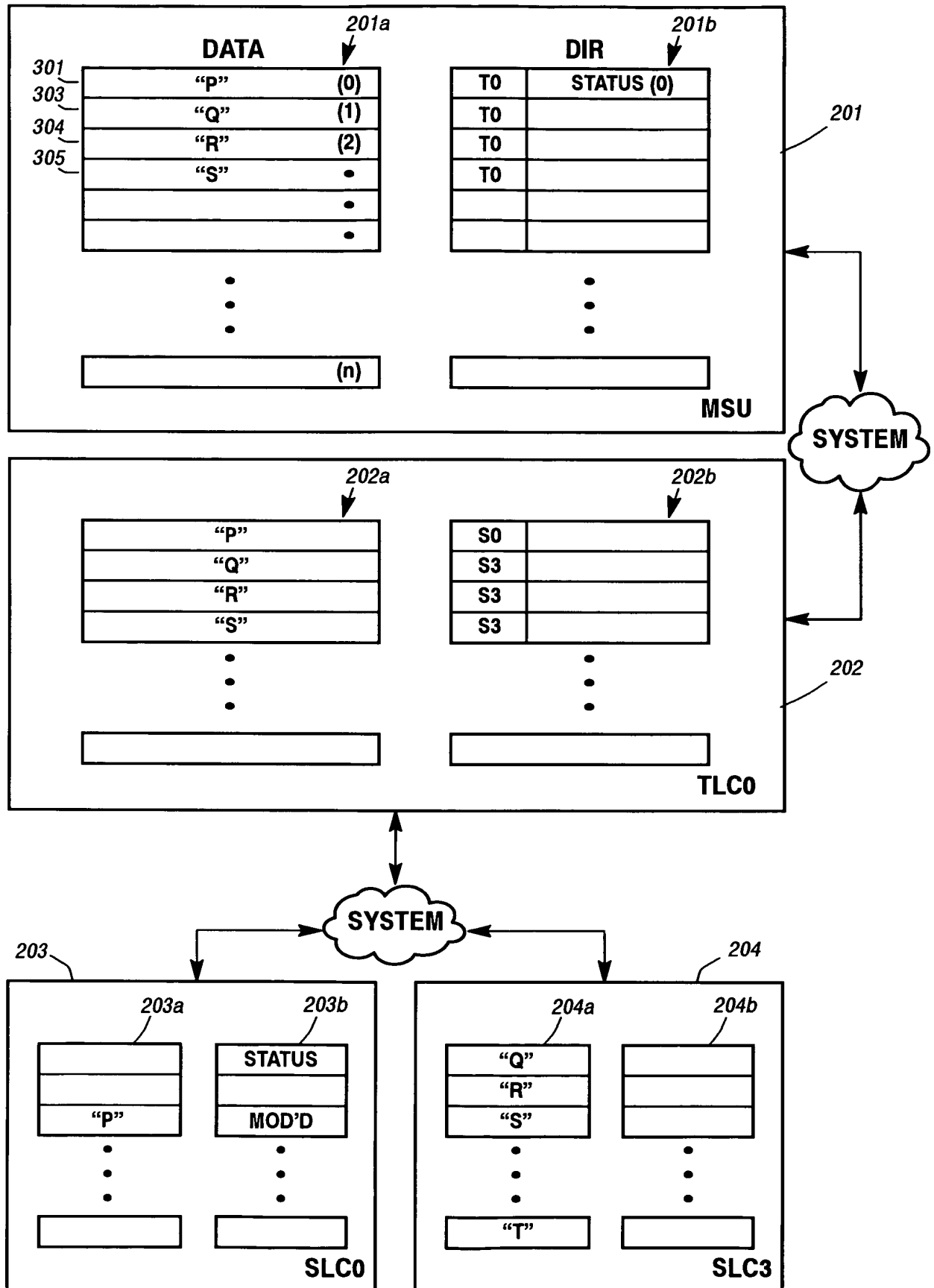


**Figure 1**

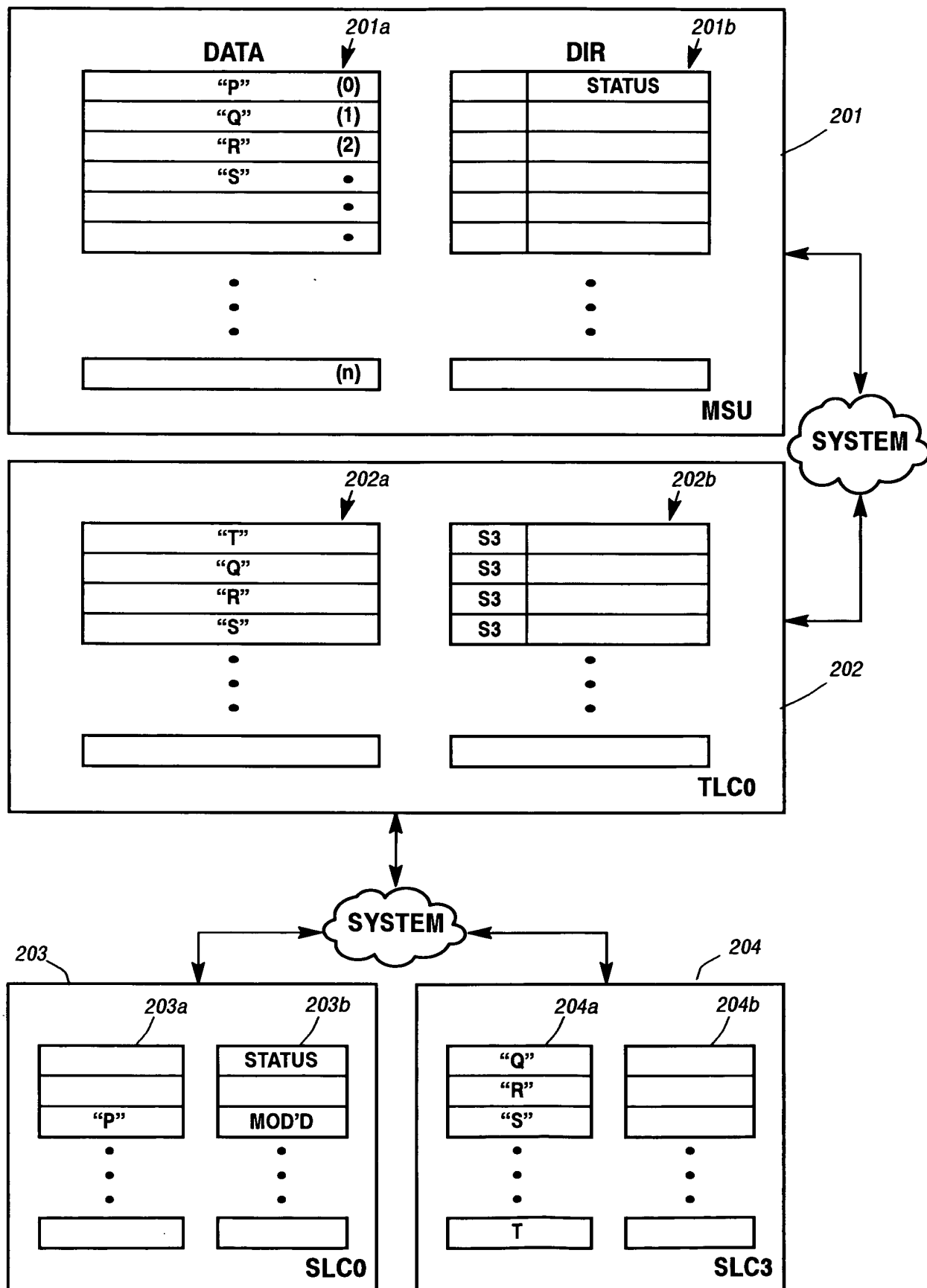
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**Figure 2**



**Figure 2A**



**Figure 2B**

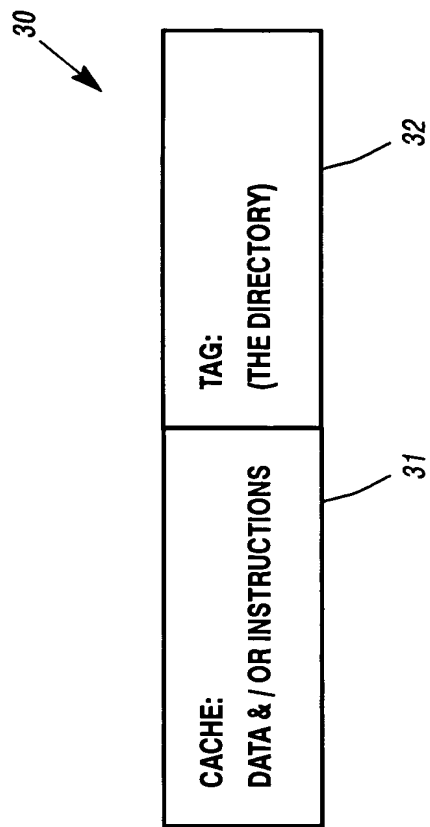
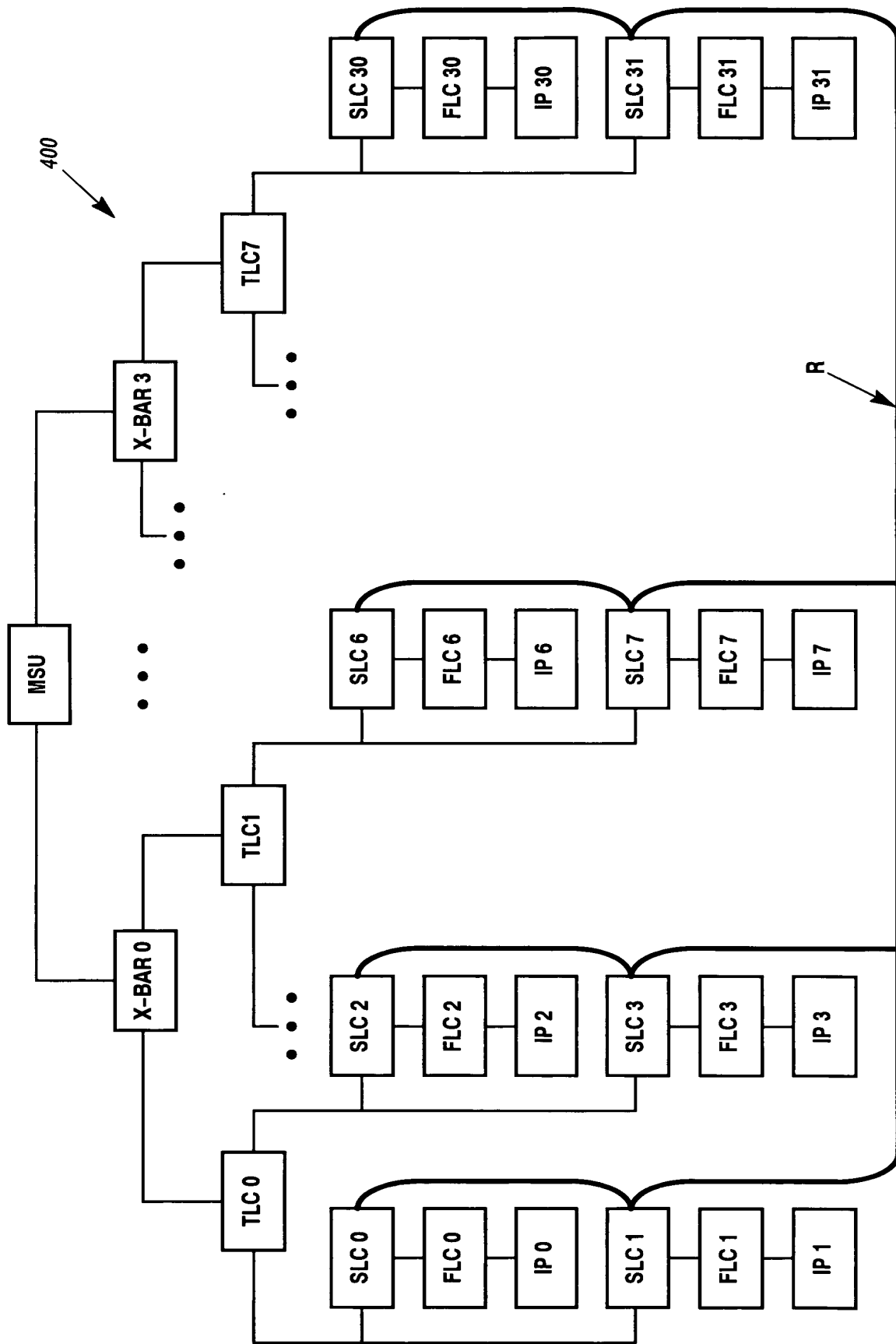
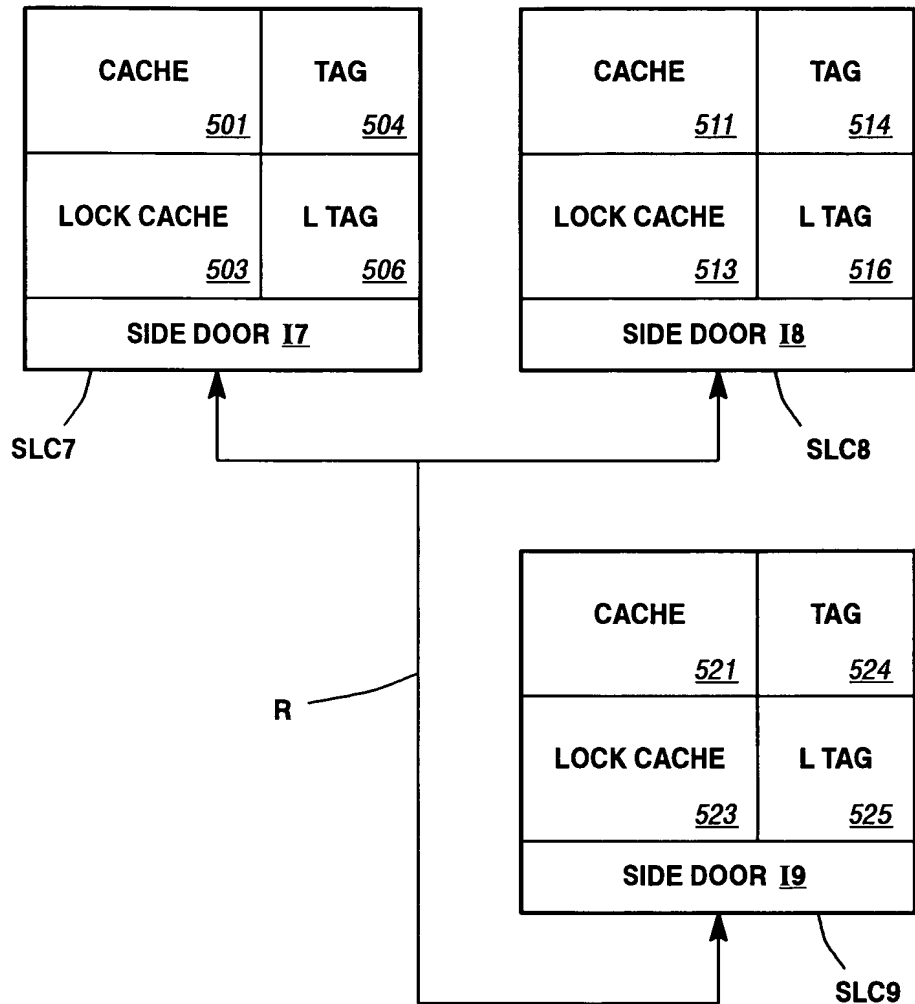


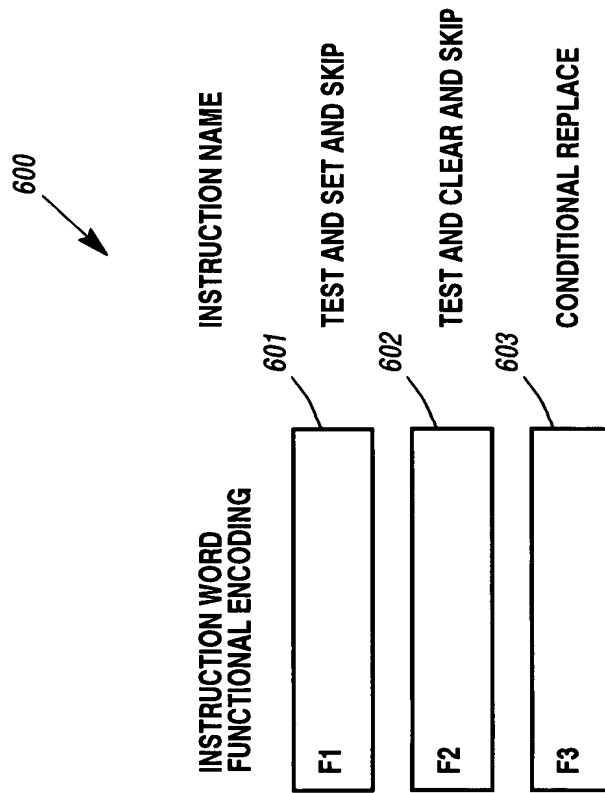
Figure 3



**Figure 4**



**Figure 5**



**Figure 6**  
***Lock Instructions***





09927069.080901

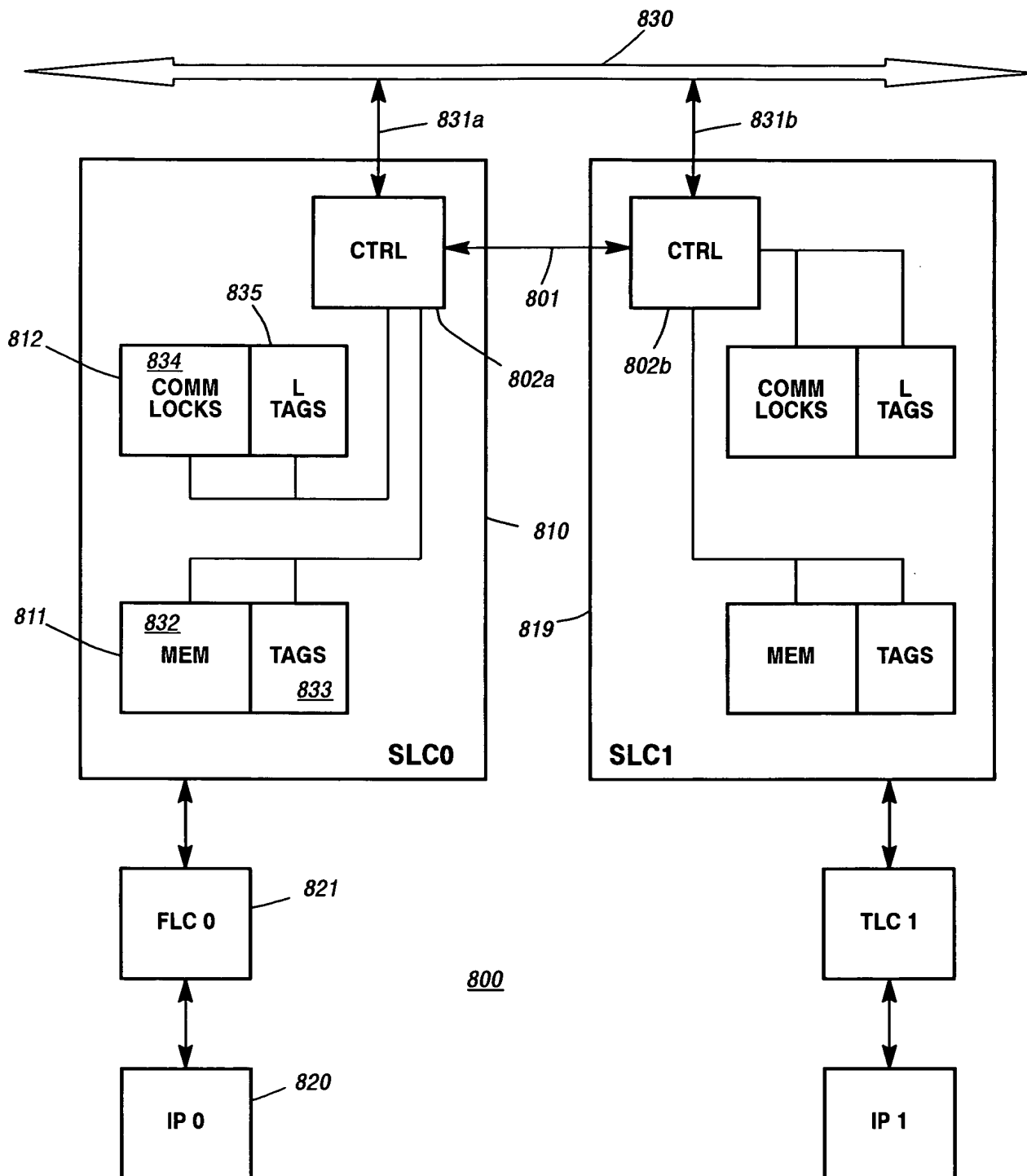
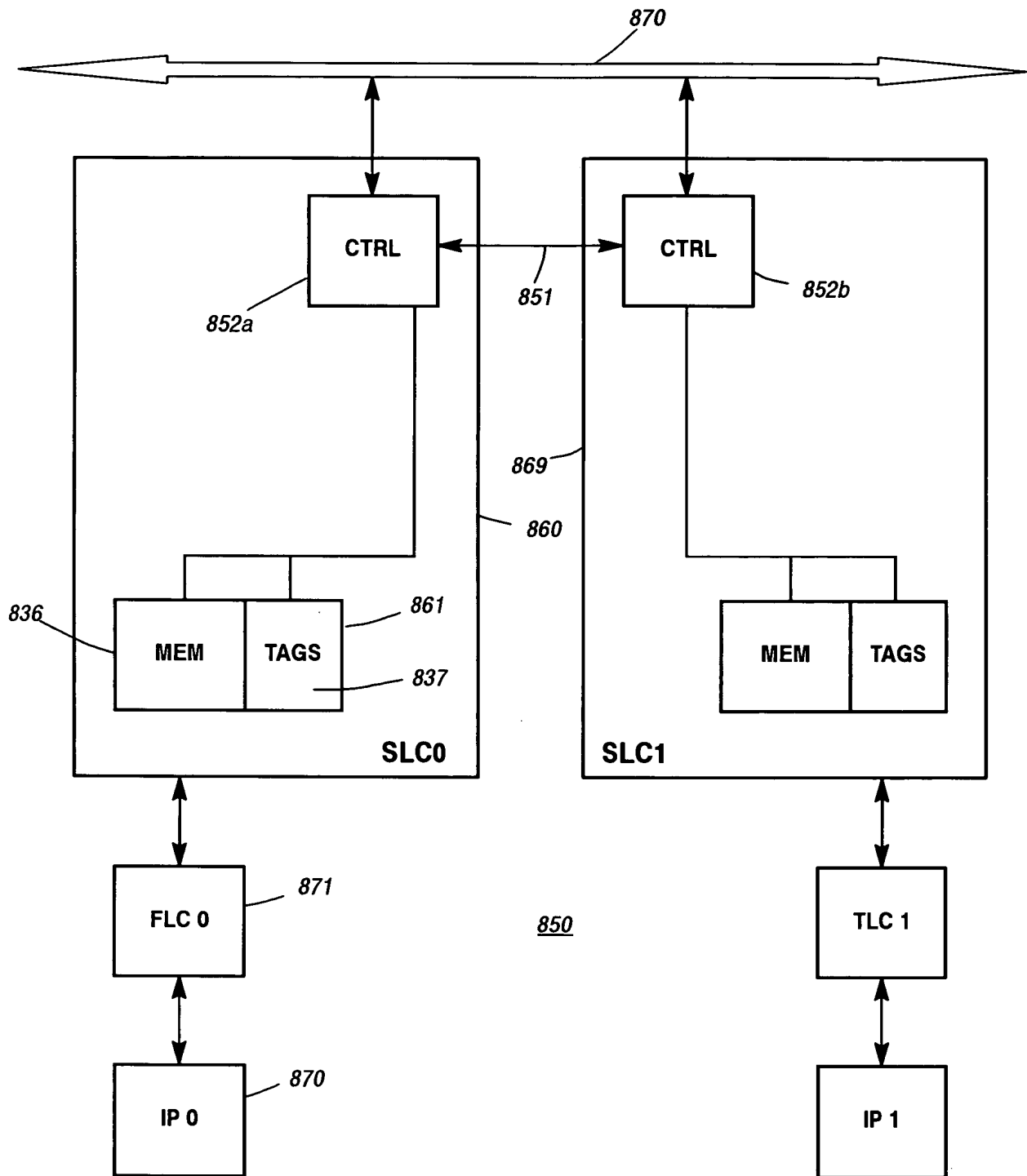


Figure 8A

09927069.080001



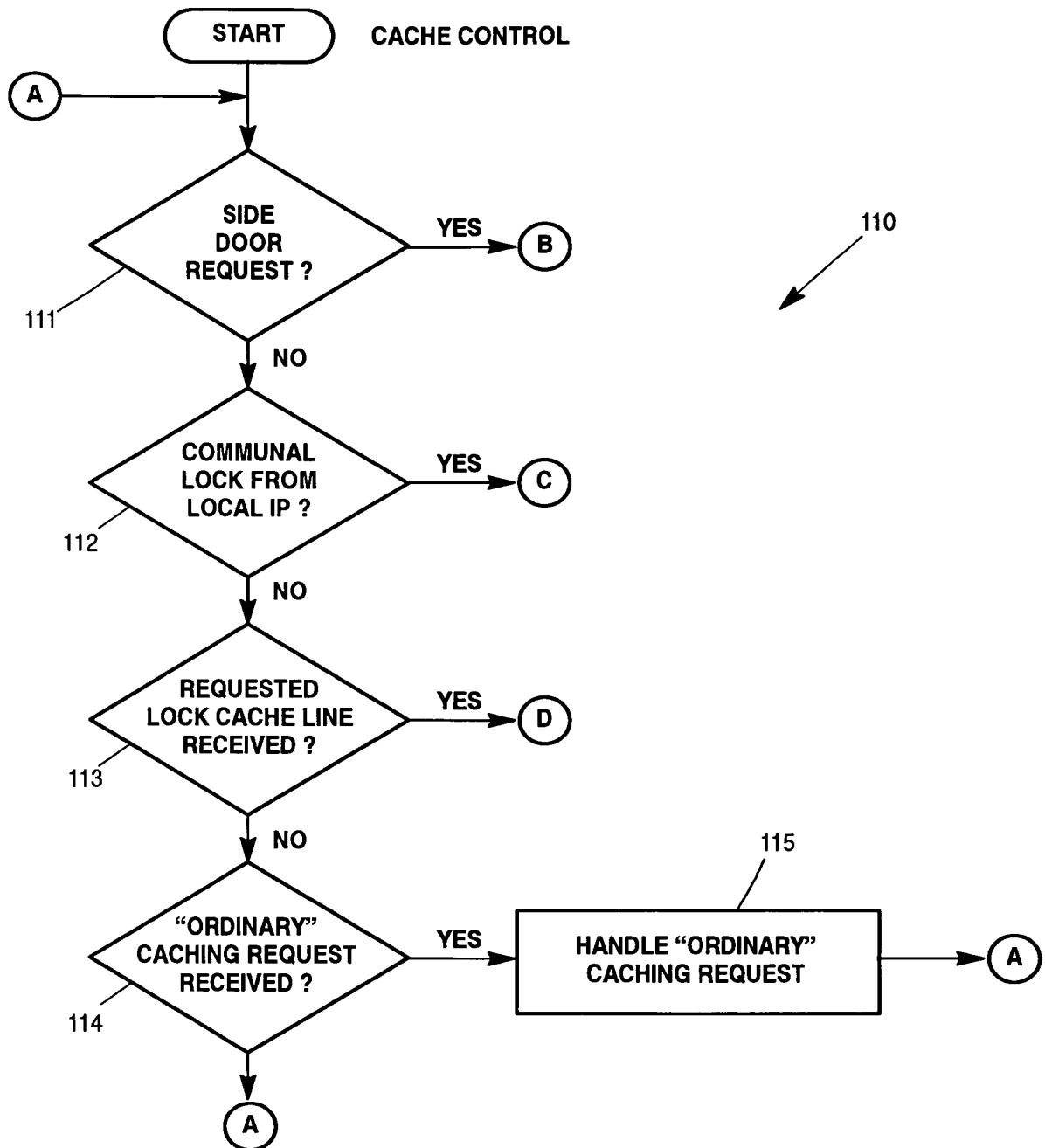
**Figure 8B**

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|     |        |        |  |
|-----|--------|--------|--|
| 700 | ADDR 1 | SLC 0  |  |
|     | ADDR 2 | SLC 0  |  |
|     | •      | SLC 7  |  |
|     | •      | SLC 7  |  |
|     | •      | SLC 15 |  |
|     |        | SLC 6  |  |
|     | ADDR X | SLC 8  |  |
|     | •      | SLC 9  |  |
|     | •      | SLC 0  |  |
|     | •      | SLC 9  |  |
|     |        | SLC 32 |  |
|     | ADDR n | SLC 32 |  |
|     |        |        | 901<br>(LD7)                               |
|     |        |        | 902  |
|     |        |        | 900<br>(SLC 7<br>COM-LOCK<br>MEM<br>ARRAY) |

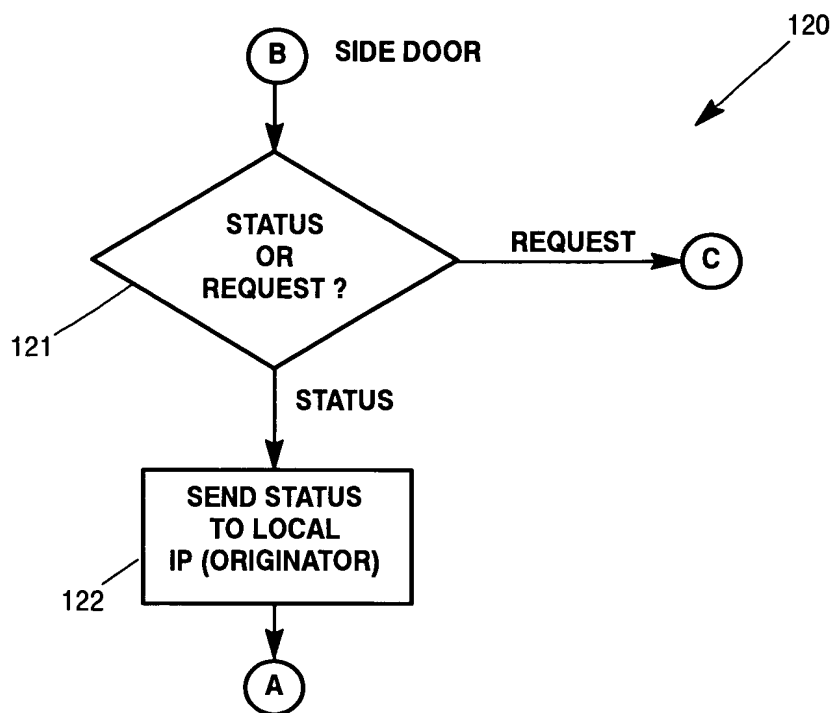
Figure 9

106080"69072660

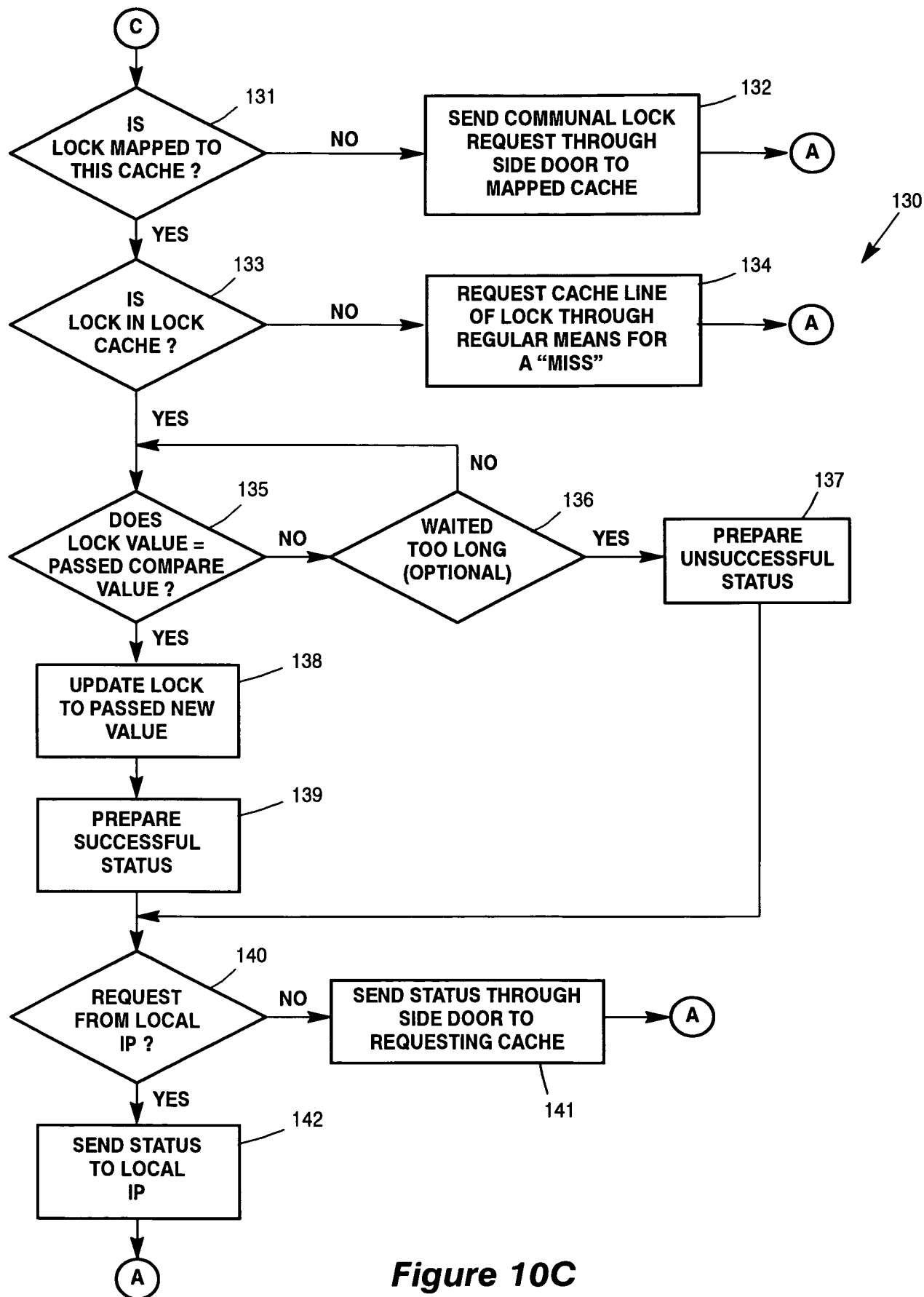


**Figure 10A**

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**Figure 10B**



**Figure 10C**

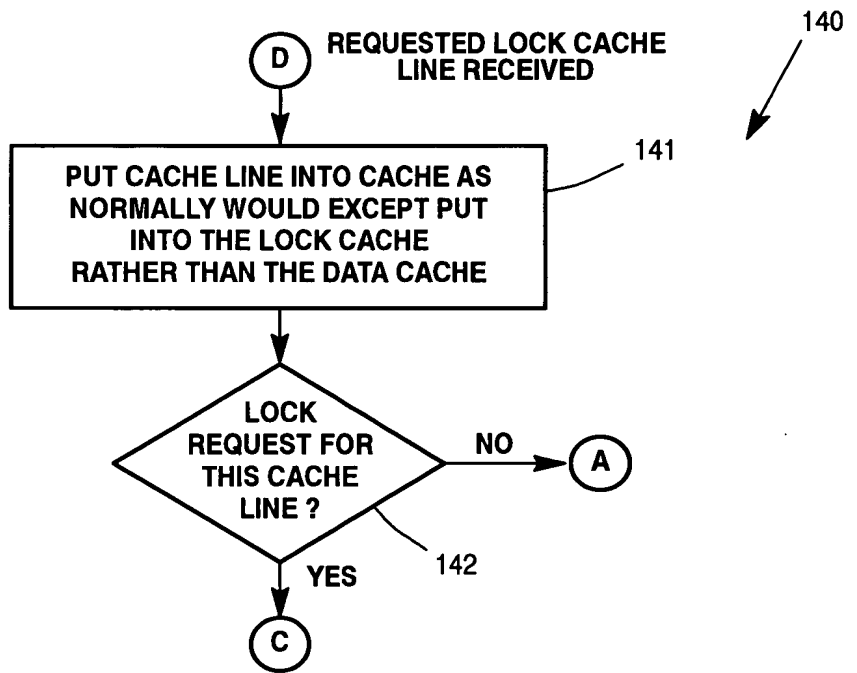
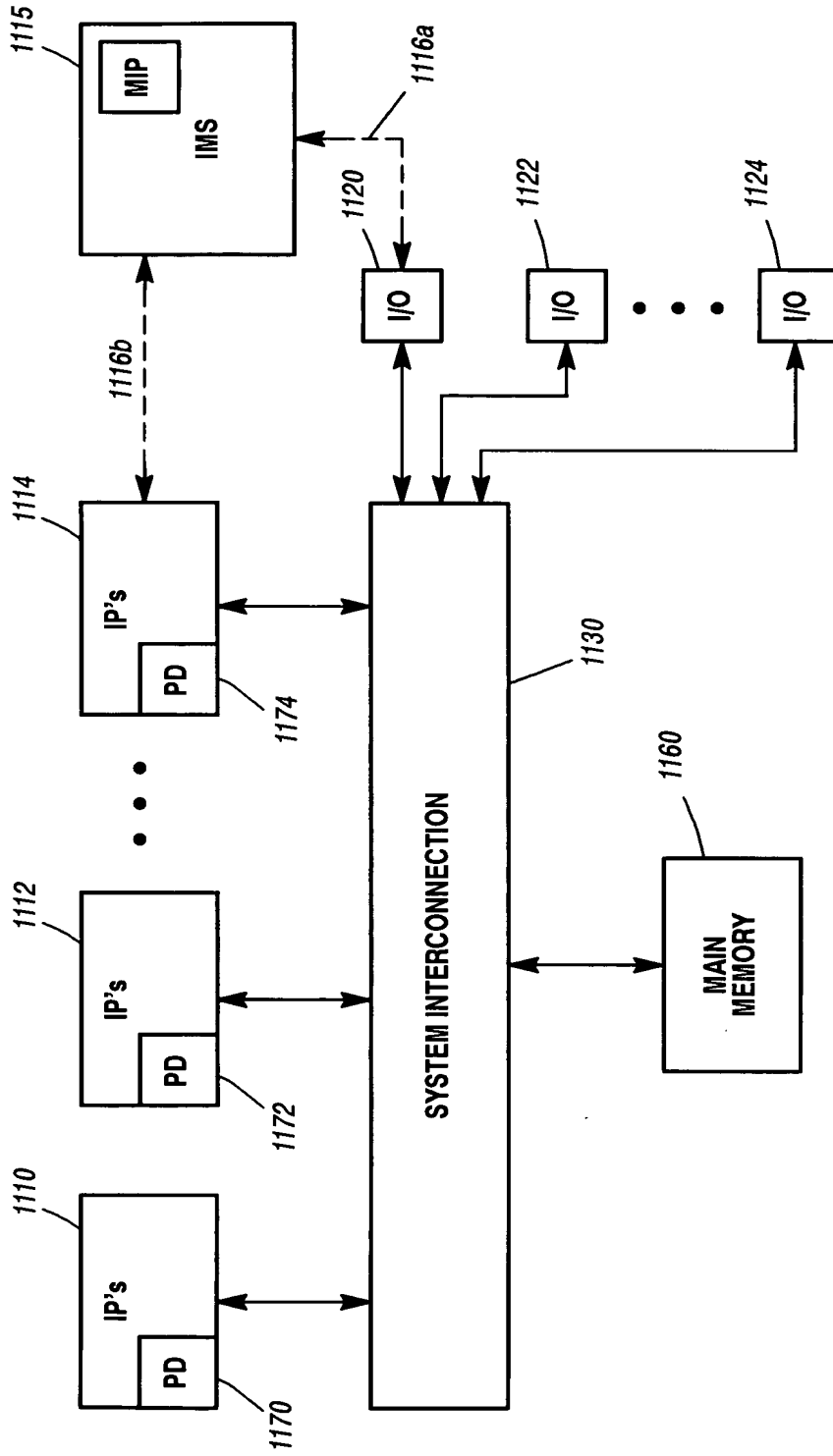


Figure 10D





**Figure 11**

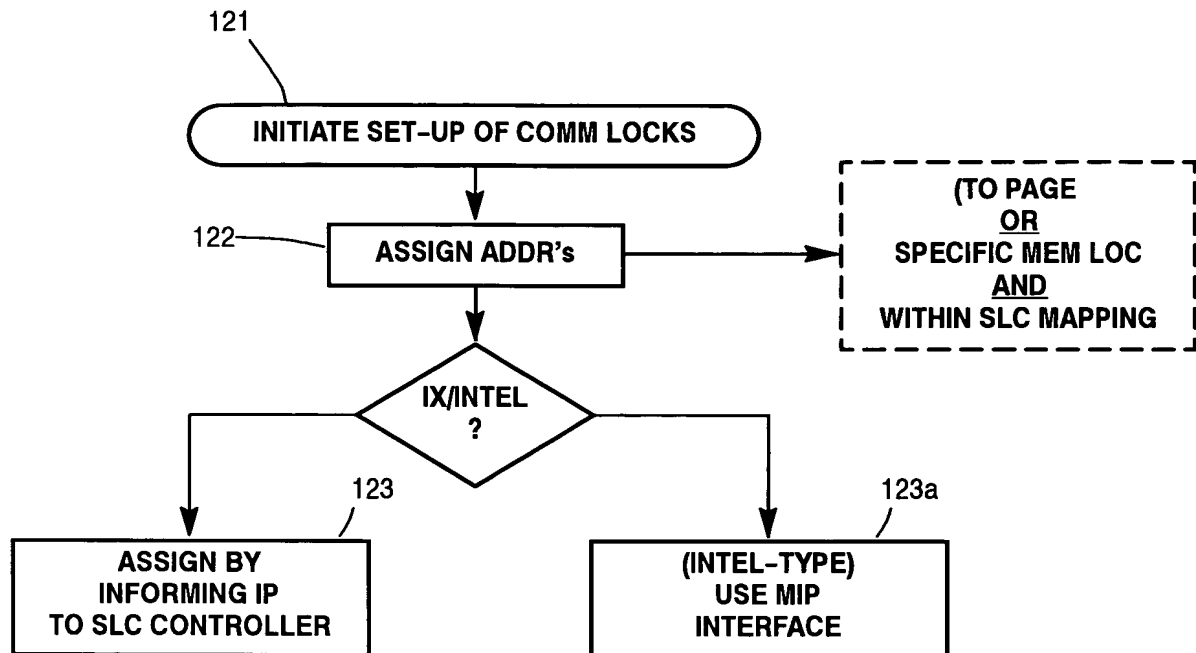
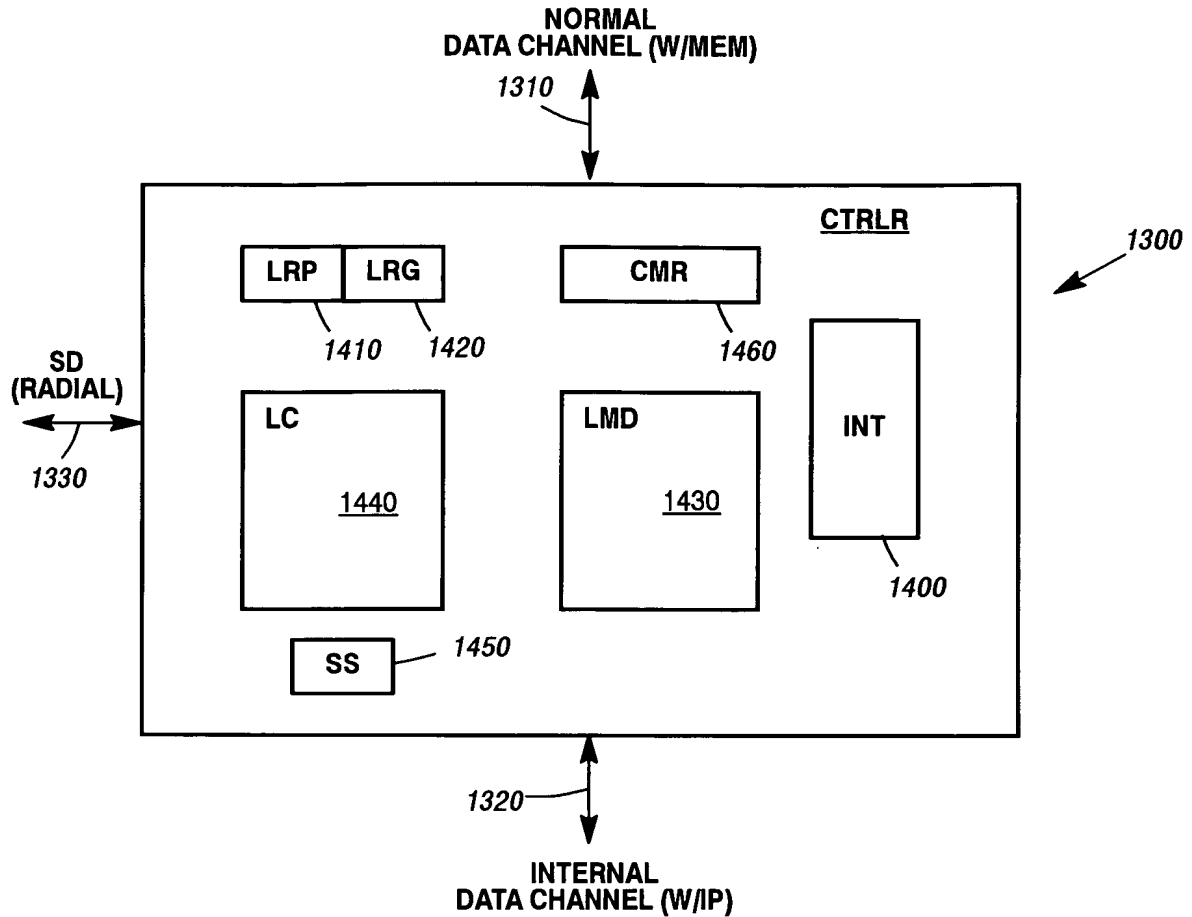


Figure 12



- LRG = LOCK REQ. GENERATOR - GENERATES / FORWARDS LOCK REQUEST IF LRP SAYS IT D/N HAVE IT.
- LRP = LOCK REQUEST (INSTRUCTION) PROCESSOR (HAS BIT CHANGER & CHECKS LMD & LC TO SEE IF (A) ITS HIS & (B) IF HE HAS IT)
- SD = SIDE DOOR (INCLUDES ID INFO PATH)
- CMR = COMPARITOR
- LMD = LOCK MAP DIRECTORY
- LC = LOCK CACHE
- INT = INTERPRETER (IS THIS A LOCK REQUEST? ELSE PASS THROUGH AS NORMAL DATA)
- SS = STATUS STRIPPER (SENDS BACK LOCK STATUS TO SIDE DOOR WHEN OPERATION COMPLETES IN RESP TO SIDE DOOR REQ)

**Figure 13**